

REMARKS

Claims 1 – 17 and 19 remain. Claims 18 and 20 are cancelled without prejudice.

103 Rejections

In the present above referenced Office Action, Claims 1-5, 7, 8, 10 – 12, 15 –17and 19 are rejected under 35 U.S.C. 103 (a) as being unpatentable over Buuck et al. in view of Chi (both of record). Applicants respectfully reassert that the present claimed invention is neither shown nor suggested by the Buuck et al., and/or Chi references, alone or together in combination.

The present Office action acknowledges that the Buuck et al. reference does not teach a termination stub that is resistive (Claim 1), including a voltage divider having particulars of Claims 2, 3, 4, 5, 7, and 16. In addition, Applicants respectfully assert that the Buuck et al. reference teaches away from the present invention by indicating a terminator 50 [Figure 1] is removed from the system [Figure 2] when “electrically substantially equidistant” devices are included. Applicants respectfully assert a person of ordinary skill in the art would not find a motivation or suggestion to utilize a reference that teaches eliminating terminations to teach a system with a termination.

Applicants respectfully assert that the secondary Chi reference does not overcome these and other shortcomings of the Buuck et al. reference. To the extent the Chi reference may show a lumped series impedance Z_s and a lumped shunt admittance as Y_o [Col 8 lines 55 to 60] in Figure 5, Applicants respectfully assert the Chi reference

does not show a first resistor in series with an input signal path and a second resistor in parallel with the input signal path. Applicants respectfully assert that Figure 5 of the Chi reference show the ground and input path coupled in parallel to both Zs and Yo. In addition, Applicants respectfully assert the Chi reference teaches away from the present claimed invention by indicating the Chi reference is directed to systems with unequal signal path lengths [Col. 1, lines 23 to 24] and different distribution conductor lengths [Col. 3, lines 4 to 5]. Furthermore, Applicants respectfully assert that a person of ordinary skill in the art would not find a motivation or suggestion to combine the Buuck et al. system that teaches away from including a terminator with the Chi reference that teaches away from utilizing equal signal path lengths to teach a present system with terminations and equal path lengths.

Applicant respectfully asserts that dependent Claims 2-7, 9 – 14, 16 - 17 and 19 are allowable as depending from allowable independent Claims.

In the present above referenced Office Action, Claims 6 and 9 are rejected under 35 U.S.C. 103 (a) as being unpatentable over Buuck et al. and Chi as applied to claims 1 and 8 above in view of Sato (all of record). Applicants respectfully assert that the present claimed invention is neither shown nor suggested by the Buuck et al. , Chi and/or Sato references, alone or together in combination.

The present Office action acknowledges that the Buuck and Chi reference does not teach the particular resistance means or that the resistance means is a trace resistance and the paths are traces in a circuit board. Applicants also respectfully assert the Buuck and Chi references do not teach a present system with terminations and

equal path lengths. Applicants respectfully assert that the secondary Sato reference does not overcome these and other shortcomings of the Chi reference. To the extent the Sato reference may mention a microstrip line on dielectric substrate [Col. 1, line 9], Applicants respectfully assert the Sato reference does not teach lines in a printed circuit board with a termination stub system as claimed in the present application. In addition, Applicants respectfully assert a person of ordinary skill in the art would not find a suggestion, motivation or teaching to combine the Chi teaching away reference with the Sato reference to teach a system or method as claimed in the present application.

In the present above referenced Office Action, Claims 13 and 14 are rejected under 35 U.S.C. 103 (a) as being unpatentable over Buuck et al. in view of Chi and further in view of Feraud et al. (all of record) Applicants respectfully assert that the present claimed invention is neither shown nor suggested by the Buuck et al., Chi and/or Feraud et al. references, alone or together in combination.

The present Office action acknowledges that the Buuck et al. and Chi references do not teach memory components/chips. Applicants respectfully assert that the secondary Feraud et al. reference does not overcome these and other shortcomings of the Buuck reference. To the extent the Feraud et al. reference may merely mention a memory [Col. 1 line14], Applicants respectfully reassert the Feraud et al. reference does not teach receivers in an integrated circuit and/or a device capable of functioning in an IC with a control signal that controls memory components. To the extent the Feraud et al. reference may mention printed circuit boards, Applicants respectfully assert the Feraud et al. reference does not teach functioning in a printed circuit board. Applicants also respectfully assert that the mere mention of a memory and or clock signal does not

teach control signals that are chip select signals. In addition, Applicants respectfully assert the Feraud et al. reference teaches away from the present invention by indicating transmission lines are unterminated [Col. 2 line 8]. Applicants respectfully assert a person of ordinary skill in the art would not find a suggestion, motivation or teaching to combine the Feraud teaching away reference with the Buck et al. teaching away reference and the Chi teaching away reference to teach a system or method as claimed in the present application.

Conclusion

In light of the above-listed amendments and remarks, Applicants respectfully request allowance of the remaining Claims. The examiner is urged to contact Applicant's undersigned representative if the Examiner believes such action would expedite resolution of the present Application.

Respectfully submitted,

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